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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/629,672
	Filing Date	July 30, 2003
	First Named Inventor	Ji Eun JANG
	Art Unit	2138
	Examiner Name	Phung M. SHUNG
Total Number of Pages in This Submission	Attorney Docket No.	40296-0032

ENCLOSURES (Check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual Name	Stephen D. Huang, Reg. No. 45,304		
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In re patent application of:
JANG, Ji Eun

Confirmation No.: 6545

Serial No.: 10/629,672

Art Unit: 2138

Filed: July 30, 2003

Examiner: CHUNG, Phung M.

For: TEST MODE CIRCUIT OF SEMICONDUCTOR MEMORY DEVICE

AMENDMENT AND REPLY UNDER 37 C.F.R. § 1.111

MAIL STOP - Amendment
Commissioner for Patents
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Sir:

Applicant herein respond to the Office Action mailed November 17, 2005, for the above-captioned application

Amendments to the Specification are reflected on page 2 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this document.

Remarks/Arguments begin on page 7 of this document.

Please amend the Application as set forth in the Amendments below.